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MICROELECTRONIC CIRCUIT DESIGN. FIFTH EDITION Richard C. Jaeger Distinguished University Professor Emeritus ECE Department Auburn University jaegerc@auburn.edu and Travis N. Blalock Visiting Associate Professor ECE Department University of Virginia blalock@virginia.edu

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Assuming the voltages are defined relative to the original S and D terminals as in Fig. 4.54 (b),  $V_{GS} = 2 - (-0.5) = 2.5 \text{ V}$ ,  $V_{GS} - V_{TN} = 2.5 - 0.7 = 1.8 \text{ V}$ , and  $V_{DS} = 0.5 \text{ V} \rightarrow$  triode region (f) The source and drain of the transistor are again reversed because of the sign change in  $V_{DS}$ .

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